

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu CHIKAGAWA et al.	Art Unit: 2841
Application No.: 10/597,919	
Confirmation No.: 7894	Examiner: J. Norris
Filing or 371(c) Date: August 11, 2006	
Title: MULTILAYER SUBSTRATE WITH BUILT-IN CHIP-TYPE ELECTRONIC COMPONENT AND METHOD FOR MANUFACTURING THE SAME	

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement issued on April 6, 2009, Applicant elects Group I, including claims 9-11 for prosecution in the present application. This election is made without traverse.

Applicant hereby reserves the right to file a Divisional Application to pursue prosecution of the non-elected claims.

Favorable consideration and prompt allowance of the present application are respectfully requested.